MUTHAYAMMAL ENGINEERING COLLEGE

(An Autonomous Institution)

(Approved by AICTE, New Delhi, Accredited by NAAC, NBA & Affiliated to Anna University)

Rasipuram - 637 408, Namakkal Dist., Tamil Nadu.

Department of Electronics Engineering (VLSI Design and Technology)

Minutes of 8th Board of Studies Meeting

Venue: ECE Research Centre   **Date: 06.04.2024**

Sl.No.   **Members**

1. **Dr.U.Saravanakumar – Chairman, Board of Studies**
   Professor & Head,
   Department of Electronics and Communication Engineering,
   Muthayammal Engineering College,
   Rasipuram – 637 408

2. **Dr. K.Rajalakshmi – University Nominee**
   Associate Professor,
   Department of Electronics and Communication Engineering,
   PSG College of Technology, Coimbatore – 641 004

3. **Dr. P.Nirmal Kumar – Academic Expert**
   Professor,
   Department of Electronics and Communication Engineering,
   CEG Campus, Anna University, Chennai – 600 025

4. **Mr.P.Chinnadurai – Industry Expert**
   Project Manager,
   Jasmine Infotech Pvt. Limited, Chennai.

5. **Mr. J.Srinivasan - Alumni**
   Depty Principal Engineer,
   Alten India, Bengaluru.

6. **Dr.B.Gopi-Internal Member**
   Professor and Dean (P&C),
   Department of Electronics and Communication Engineering,
   Muthayammal Engineering College,
   Rasipuram-637408

7. **Dr.T.Ganesh Babu-Internal Member**
   Professor,
   Department of Electronics and Communication Engineering,
   Muthayammal Engineering College,
   Rasipuram-637408

8. **Dr.T.Kowsalya-Internal Member**
   Professor,
   Department of Electronics and Communication Engineering,
   Muthayammal Engineering College,
   Rasipuram-637408

9. **Dr.J.Rangarajan-Internal Member**
   Professor,
   Department of Electronics and Communication Engineering,
   Muthayammal Engineering College,
   Rasipuram-637408

10. **Dr.C.Selvi - Internal Member**
    Associate Professor,
    Department of Electronics and Communication Engineering,
    Muthayammal Engineering College,
    Rasipuram-637408

11. **Dr.R.Praveena- Internal Member**
    Associate Professor,
    Department of Electronics and Communication Engineering,
    Muthayammal Engineering College,
    Rasipuram-637408

12. **Dr.R.Prakash - Internal Member**
    Professor,
    Department of Electrical and Electronics Engineering,
    Muthayammal Engineering College, Rasipuram-637408

13. **Dr.D.Sasikala- Internal Member**
    Professor and Head,
    Department of Biomedical Engineering,
    Muthayammal Engineering College,
    Rasipuram-637408

Minutes of Meeting

- The Chairman of the BoS welcomed all the members for the 8th Board of Studies Meeting.

- The Anna University Nominee Suggested that all the mandatory courses need to be included in the curriculum with reference to UGC Guidelines and AICTE model Curriculum.

- The BoS members discussed the scheme, curriculum and syllabi of the B.E. Electronics Engineering (VLSI Design and Technology) programme in the baskets of Humanities and Social Sciences, Basic Science and General Engineering Science Courses under Regulations 2023.

- BoS members discussed the Curriculum considering the guidelines of UGC, AICTE, Premier Institutions, Foreign Universities, PEOs, POs and PSOs of the program and the views of the stakeholders.

- Curriculum and syllabi suggested by Curriculum Development Committee (CDC) are reviewed and recommended.

- It was recommended to maintain the total credit to be earned as 161.

- BoS members suggested that, Basic Science paper of UG may include Numerical Methods courses under Regulations 2023.

- BoS members suggested that, the three Internship may be reduced two Internship which is sufficient for courses under Regulations 2023. It is suggested to identify more industries for internship programs. It is also suggested to include innovative practices in 4th semester for improving cognitive thinking.

- BoS members suggested to include Communication Systems in Professional Electives.

- BoS members suggested to include Analog IC laboratory in 5th Semester.

- It is suggested to rename the course title Analog and Mixed Signal IC into Mixed Signal IC.

- Members suggested to use latest EDA tools and FPGA boards for experiments.

- Members suggested to include complete IC flow from design entity to chip tape out.

- The BoS members recommended to include interdisciplinary courses as open elective in semester V, VI and VII.

- BoS members suggested to use Open Source EDA tools for experiments and Mini Project.

- BoS members also suggested to verify the credit distribution and laboratory course offered in each semester.
- Reviewed the CO attainment calculation and the CO attainment target for theory courses and for practical courses. PO/PSO attainment target was also discussed.
- The BoS members suggested to plan for core company industrial visits.
- Discussed the Value Added Courses, Employability Courses, Entrepreneurship and Skill Development Courses which were suggested by the stakeholders.
- Reviewed and approved the panel members for project viva, question paper setting and evaluation (Attached in Annexure I).
- Finally, the Chairman of the Board thanked the entire member for their valuable suggestions.

Chairman
Board of Studies