



MUTHAYAMMAL ENGINEERING COLLEGE

(An Autonomous Institution)

(Approved by AICTE, New Delhi, Accredited by NAAC & Affiliated to Anna University)

Rasipuram - 637 408, Namakkal Dist., Tamil Nadu.

Department of Computer Science and Engineering Question Bank - Academic Year (2021-22)

Course Code & Course Name : 19CSC05-Computer Organization and Architecture
Name of the Faculty : T.ARAVIND
Year/Sem/Sec : II / III / B

Unit-I: Basic Structure Of Computers

Part-A (2 Marks)

1. Define the term Computer Architecture.
2. What is program controlled I/O.
3. What is Bus?
4. Define Pipeline processing.
5. Draw the basic functional units of a computer.
6. Define clock rate.
7. What is register?
8. Write the basic performance equation?
9. Write down the operation of control unit?
10. Define Addressing Modes

Part-B (16 Marks)

1. Draw and explain block diagram of simple computer with the functional units. (16)
2. Why do we use addressing modes ? Explain the different types of Addressing modes with example (16)
3. Explain the various techniques meant for improving the performance of a computer (16)
4. Write a program for Instruction and instruction sequencing with its operation (16)
5. What is bus explain it in detail? (16)

Unit-II : Arithmetic Unit

Part-A (2 Marks)

1. Draw the half adder circuit and give the truth table.
2. Write the logic equations of a binary half adder
3. State the rules of floating point multiplication.

4. How bit pair recoding of multiplier speeds up the multiplication process?
5. What is a ripple carry adder?
6. State the principle behind the Booth's multiplier
7. State the role of Booth's algorithm in the design of fast multipliers
8. Define IEEE floating point single and double precision standard
9. In conforming to the IEEE standard mention any four situations.
10. What is floating point number system?

Part-B (16 Marks)

1. Explain in detail the principle of carry-look-ahead adder with a neat Sketch. (16)
2. Describe the algorithm to multiply two positive numbers. Also discuss the realization of a multiplier to implement the same. (16)
3. Illustrate Booth's algorithm with an example. (16)
4. (i). Demonstrate the division of 1100_2 by 101_2 using restoring method, draw block diagram and explain the operation (6)
- (ii). Explain the Representations of Floating point numbers in detail (10)
5. State and Explain the rules in arithmetic operations on floating point numbers. (16)

Unit-III : Basic Processing Unit And Pipelining

Part-A (2 Marks)

1. Draw the four stages in the pipelining
2. Compare Instruction Pipelining and hazard in pipelining
3. State instruction or control hazard.
4. Difference between delayed load and delayed slot.
5. What would be the effect, if we increase the number of pipelining stages?
6. List out the classification of data hazards?
7. Give an example on the instruction pipelining?
8. Why is branch prediction algorithm needed?
9. Write a note branch target buffer?
10. Differentiate between the static and dynamic branch prediction.

Part-B (16 Marks)

1. Explain basic operation of a Four Stage pipelining with a neat diagram (16)
- 2.(i). Discuss the various hazards that might arise in a pipeline (8)
- (ii). Explain the Issues affecting pipeline performance (8)
3. What is a Data hazard? How do you overcome it? And discuss its side effects. (16)
4. (i). Describe the methods for dealing with the branch hazards . (8)
- (ii). Discuss in detail about hazards due to unconditional branching. (8)

5. What are the hazards of conditional branches in pipelines? How it can be resolved? (16)

Unit-IV : Memory System

Part-A (2 Marks)

1. What will be the width of address and data buses for a 512 K * 8 memory chip?
2. Distinguish between static RAM and dynamic RAM.
3. Give the features of a ROM.
4. Compare SDRAM with DDR SDRAM
5. How do you construct a 8 m * 32 memory using 512 K *8 memory chips?
6. What is double data rate SDRAMs?
7. Define locality of reference. What are its types?
8. List the mapping techniques.
9. Write a note by interleaved memory?
10. An address space is specified by 24-bits and the corresponding memory space by 16 bits: How many words are there in the virtual memory and main memory?

Part-B (16 Marks)

1. (i) Analyze the memory hierarchy in terms of speed, size and Cost. (8)
(ii) Draw the block diagram of Virtual memory and explain. (8)
2. Explain the need for cache memory and Discuss the various mapping techniques used in cache memories. (16)
3. Write about mapping function? What are the ways the cache can be mapped? (16)
4. Illustrate the different types of mapping functions in cache memory (16)
5. What is virtual memory? Explain how the logical address is translated into physical address in the virtual memory system with a neat diagram. (16)

Unit-V : Input/Output Organization

Part-A (2 Marks)

1. What is the use of DMA.
2. Specify the different I/O transfer mechanisms available.
3. What does isochronous data stream means?
4. List out the Input and output devices.
5. Give the features of PCI.
6. Compare SCSI and USB.
7. State Interrupt and ISR.
8. Draw the memory hierarchy of memory system.
9. What are the units of interface?
10. Write a short notes on Buses and its types

Part-B (16 Marks)

1. Describe the various mechanism for accessing I/O devices. (16)
2. (i). What is priority interrupt? (8)
(ii). Explain the interrupt priority schemes. (8)
3. Explain the different types of interrupts and the different ways of handling interrupts. (16)
4. (i) . With a neat sketch explain the working principle of DMA. (8)
(ii). Explain how DMA transfer is accomplished with a neat diagram. (8)
5. Write an elaborated note on PCI, SCSI and USB bus standards. (16)

Course Faculty

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